

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/146,842
Priority Filing Date September 3, 1998
Inventor Zhiping Yin et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2824
Priority Examiner B. Smith
Attorney's Docket No. MI22-1427
Title: Circuitry and Gate Stacks (As Amended)



INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56.

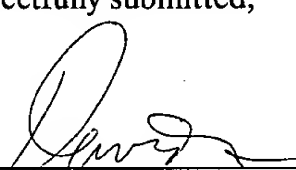
No admission is made regarding whether all the submitted references are prior art.

All listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional of co-pending Application Serial No. 09/146,842, filed September 3, 1998. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 4/26/00

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